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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/701,420	01/27/2000	Katsunori Kumasaka	0694-127	9104		
. 75	590 02/20	2002				
Hopgood Calimafde			EXAMI	EXAMINER		
60 East 42nd Street New York, NY 10165			BUDD, MARK	BUDD, MARK OSBORNE		
			ART UNIT	PAPER NUMBER		
			2834 DATE MAILED: 02/20/2002	9		

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No. 701 4 70	Applicant(s) Kumasaka et il	
Examiner M. Bs 2	Group Art Unit より3イ	

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Office Action Summary	Examiner M. B. d)		Group Art Unit			
The MAILING DATE of this communication appears	on the cover sheet b	eneath the co	orrespondence a	ddress		
Period for Response	マ					
A SHORTENED STATUTORY PERIOD FOR RESPONSE IS SET MAILING DATE OF THIS COMMUNICATION.			H(S) FROM THE			
<ul> <li>Extensions of time may be available under the provisions of 37 CFR 1.13 from the mailing date of this communication.</li> <li>If the period for response specified above is less than thirty (30) days, a least NO period for response is specified above, such period shall, by defaulted to respond within the set or extended period for response will, by</li> </ul>	response within the statuto	ory minimum of th	nirty (30) days will be g date of this commu	considered timely. nication .		
Status 10 - 1 - 0	2					
Status   10 - 1 - 0    Responsive to communication(s) filed on	<i>-</i>			•		
This action is FINAL.						
☐ Since this application is in condition for allowance except fo accordance with the practice under Ex parte Quayle, 1935	or formal matters, <b>pros</b> C.D. 1 1; 453 O.G. 21	ecution as to 3.	the merits is clo	esed in		
Disposition of Claims						
✓ Claim(s)		is/are	pending in the ap	plication.		
Of the above claim(s)		is/are	is/are withdrawn from consideration.			
□ Claim(s)		is/are	allowed.			
□ Claim(s)		is/are	$\_$ is/are rejected.			
	□ Claim(s)					
	are su	are subject to restriction or election				
		requir	rement.			
Application Papers	- · PTO 040					
☐ See the attached Notice of Draftsperson's Patent Drawing	Review, P10-948.	dicapprove	ed.			
☐ The proposed drawing correction, filed on	isapproved	uisappiove	su.			
☐ The drawing(s) filed on is/are objected	to by the Examiner.					
☐ The specification is objected to by the Examiner.						
☐ The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. § 119 (a)-(d)						
<ul> <li>□ Acknowledgment is made of a claim for foreign priority und</li> <li>□ All □ Some* □ None of the CERTIFIED copies of the</li> </ul>	der 35 U.S.C. § 11 9(a he priority documents l	)-(d). have been				
<ul> <li>received.</li> <li>received in Application No. (Series Code/Serial Numbe</li> <li>received in this national stage application from the Intel</li> </ul>	r) rnational Bureau (PCT	Rule 1 7.2(a)	 ).			
*Certified copies not received:						
Attachment(s)						
☐ Information Disclosure Statement(s), PTO-1449, Paper No.	o(s)	Interview Sur	nmary, PTO-413			
□ Notice of References Cited, PTO-892		☐ Notice of Informal Patent Application, PTO-152				
☐ Notice of Traftsperson's Patent Drawing Review, PTO-948	<b>3</b>	Other				
	Action Summary					
Office	, Auton Gammary			<u> </u>		

U. S. Patent and Trademark Office PTO-326 (Rev. 3-97)

Part of Paper No.

Art Unit: 2834

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1-9 and 12-17 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. There is no written description or illustration of a transformer in which the electrodes on the side surfaces (e.g. applicants #21, 23, #27, #29 etc) extend along approximately one-half of the longitudinal length of the transformer. As shown in applicants figs. 1, 3A, 4 and 5 the side electrodes do not extend anywhere near one-half of the transformer length. Note that claims 16 contradicts parent claim in that one cannot have an electrode extending one-half the transformer length and at the same time have multiple pairs of electrodes occupying the same area.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-4, and 7-9 are rejected under 35 U.S.C. 102(a) as being anticipated by Japan (033).

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The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 5 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Kanayama, Yamamoto or Sato in view of Japan (033), Japan (327) or Prior art (applicants fig. 1).

Kanayama (fig. 23), Sato (Figs. 2, 3 & 14) and Yamada (figs. 4 & 5) teach the piezoelectric transformer with multiple pairs of second (output) electrodes. They do not teach the side leads for the drive section or mounting on a PCB with power supply circuitry. However each of Japan (033), Japan (237) and the Prior Art (applicants figs ) teach that side leads are conventionally used-especially for laminated structures.

Japan (033) also teaches mounting a transformer on a PCB with associated input/output circuitry. Thus to use side leads for their conventional advantages and to provide integration of circuit element for Sato, Kanayama or Yamamoto would have been obvious to one of ordinary skill in the are.

Claims 10 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inou, Sakarui or Shimizu...

The references teach supporting a piezoelectric transformer in an elastic manner. They do not teach the specific mounting location. However, optimization of a known device (e.g. thru

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routine experimentation) has long been held to be within the skill expected of the routineer.

Thus selection of specific mounting locations would have been obvious to one of ordinary skill in the art.

claims 12-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamamoto in view of Japan (033) as applied to claim 5 above, and further in view of Inoi.

The combination of Yamamto and Japan (033) has been previously discussed. Neither patent teaches using an elastomeric mount for a piezoelectric transformer. However, such a mount is well known in conjunction with piezoelectric transformers as taught by Inoi. A flexible mount protects the ceramic from harm and isolates it from vibrations in a well known manner. Therefore to use an elastic mount for Japan (033) or Yamamoto for its known benefits would have been obvious to one of ordinary skill in the art. The use of flat, flex cable connectors (known per se) is considered within the skill expected of the routineer. Such choice amounts to selection from among known connectors and would have been obvious to one of ordinary skill in the art.

Regarding applicants arguments it is noted that Japan (033) figs. 3, 4 and 8 clearly teaches electrodes on both side surfaces of the transformer. As for as claims 10 and 11 go, it has long bene held that optimization of a known device e.g. thru routine experimation is within the skill expected of the routineer. Selection of mounting areas, usually nodal locations can easily be optimized for a particular application via e.g. trail and error.

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THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time

policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE

MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after

the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event will

the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Budd/ds

02/16/02

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